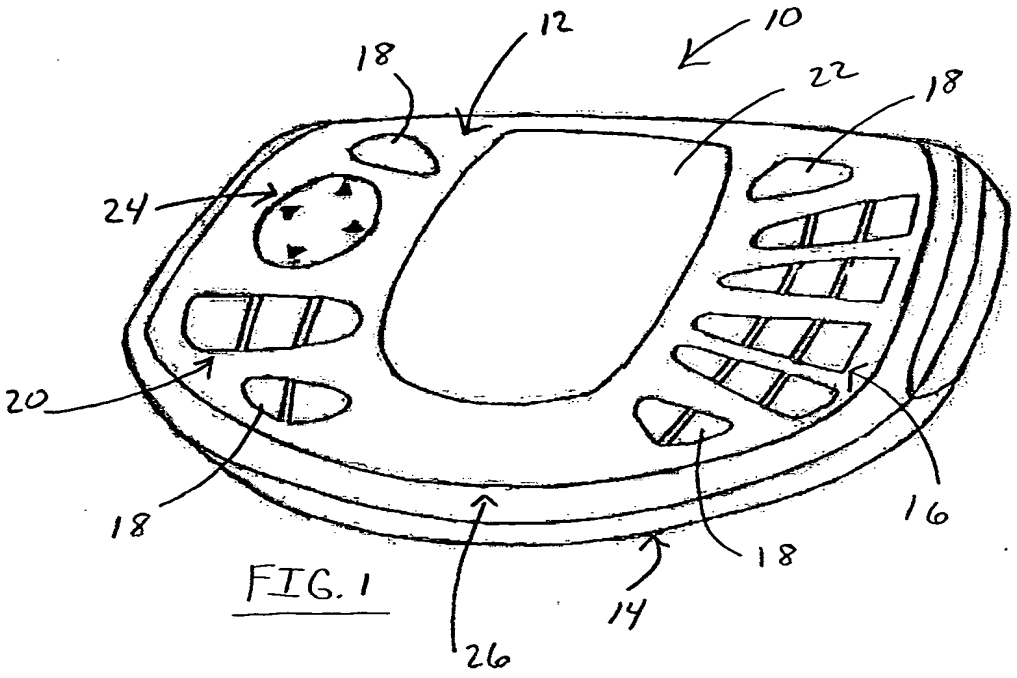



FIG. 1

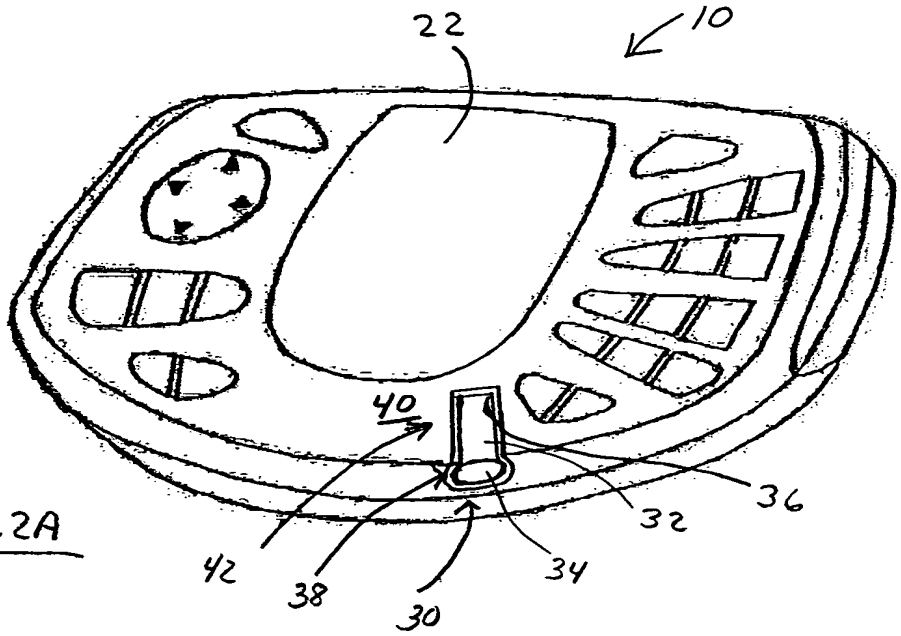



FIG.2A

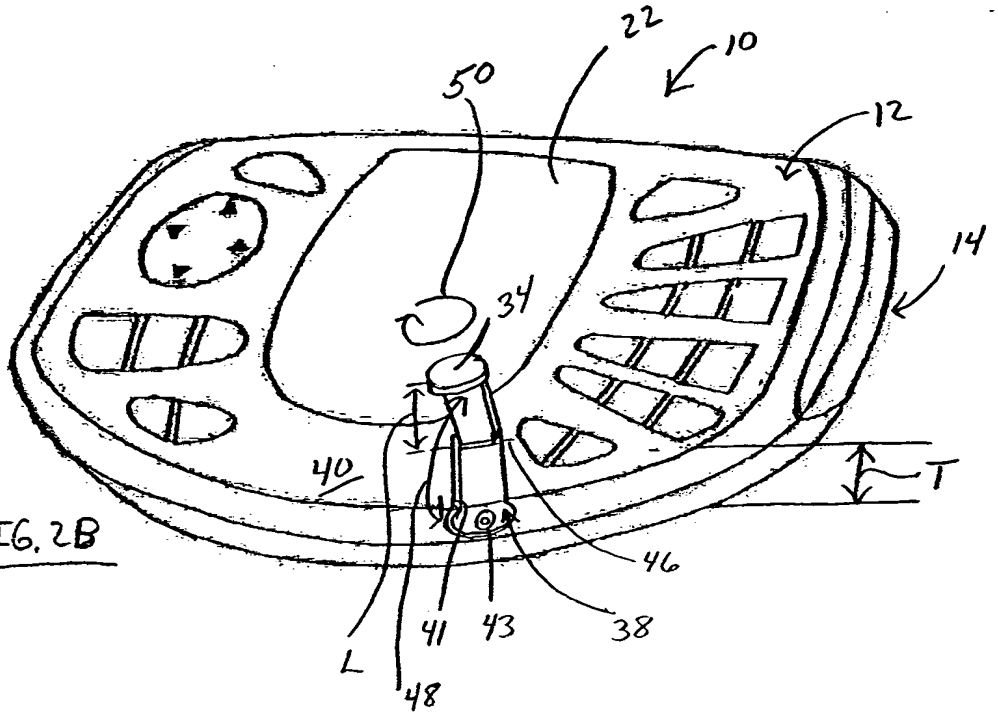



FIG. 2B

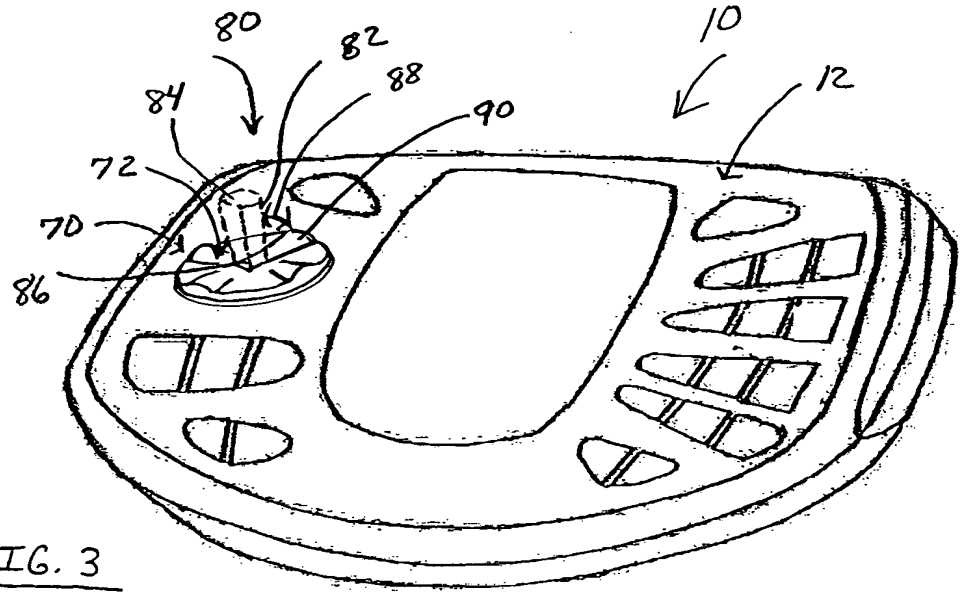



FIG. 3

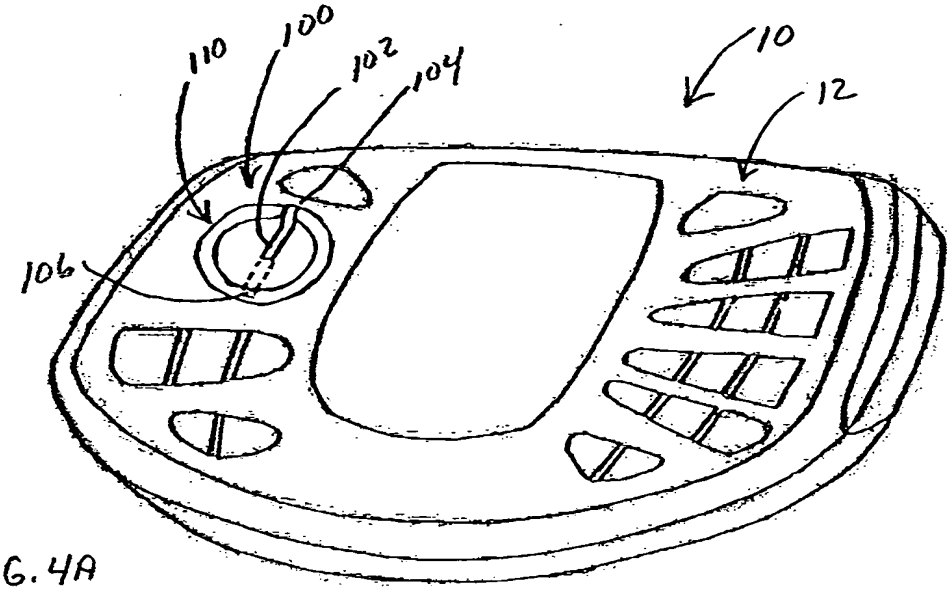



FIG. 4A

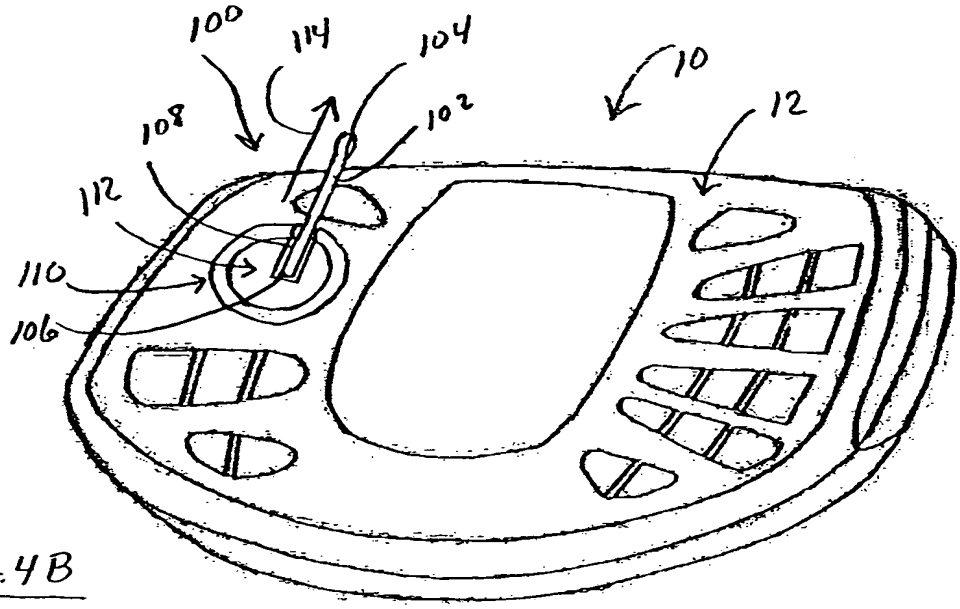



FIG. 4B

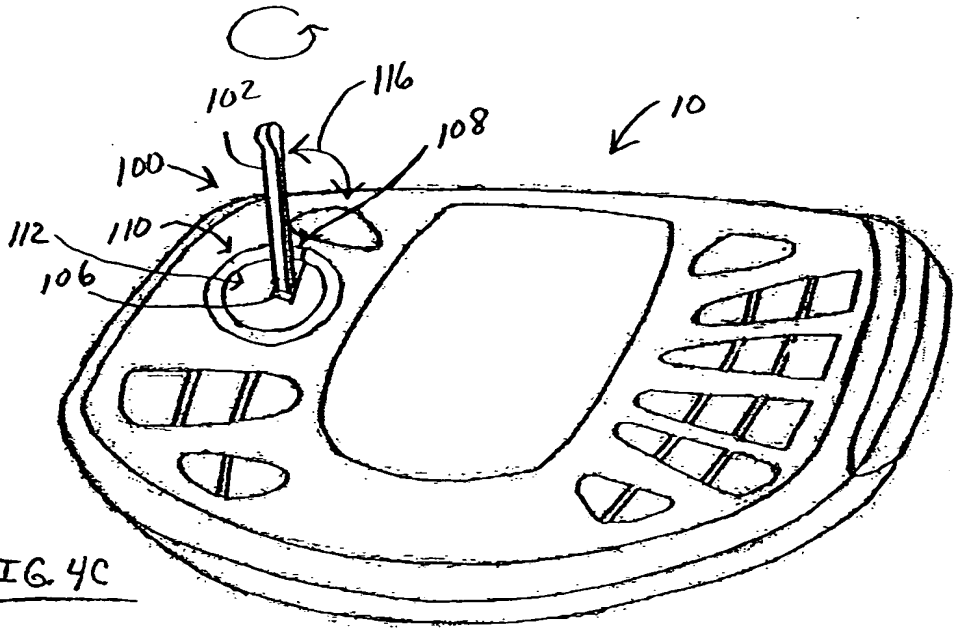



FIG. 4C

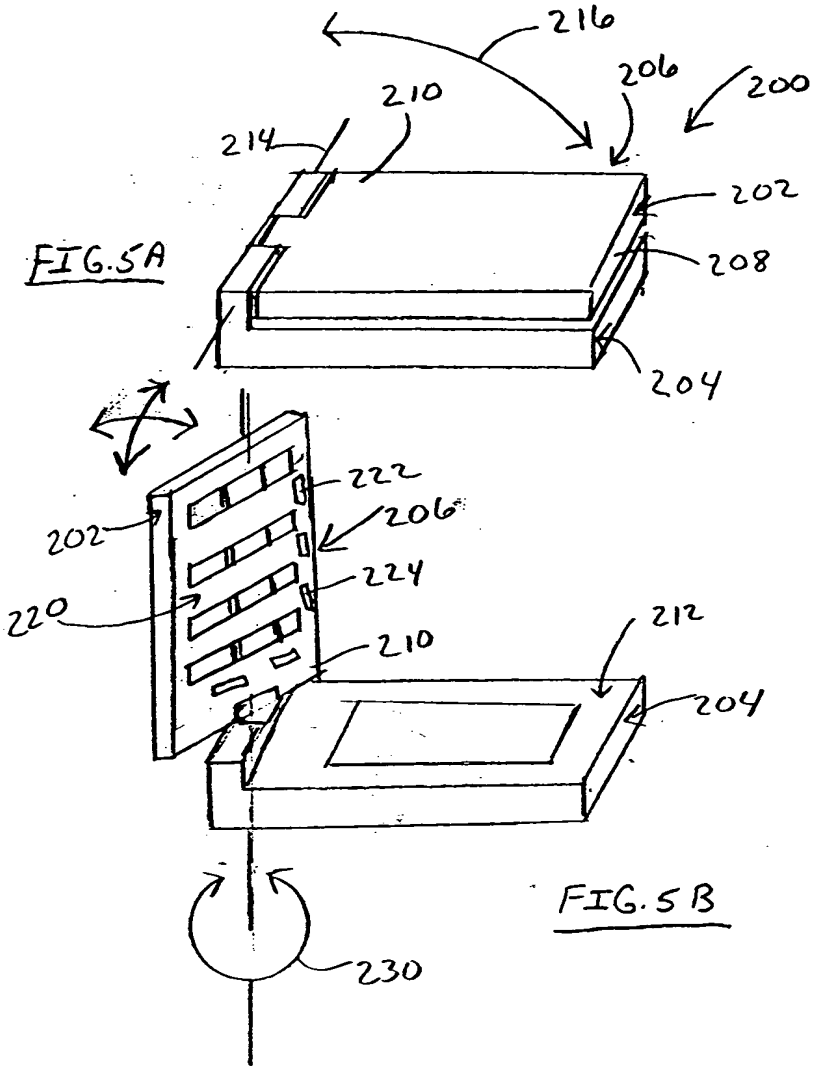

216
206
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212
204
230

FIG. 5A

FIG. 5B

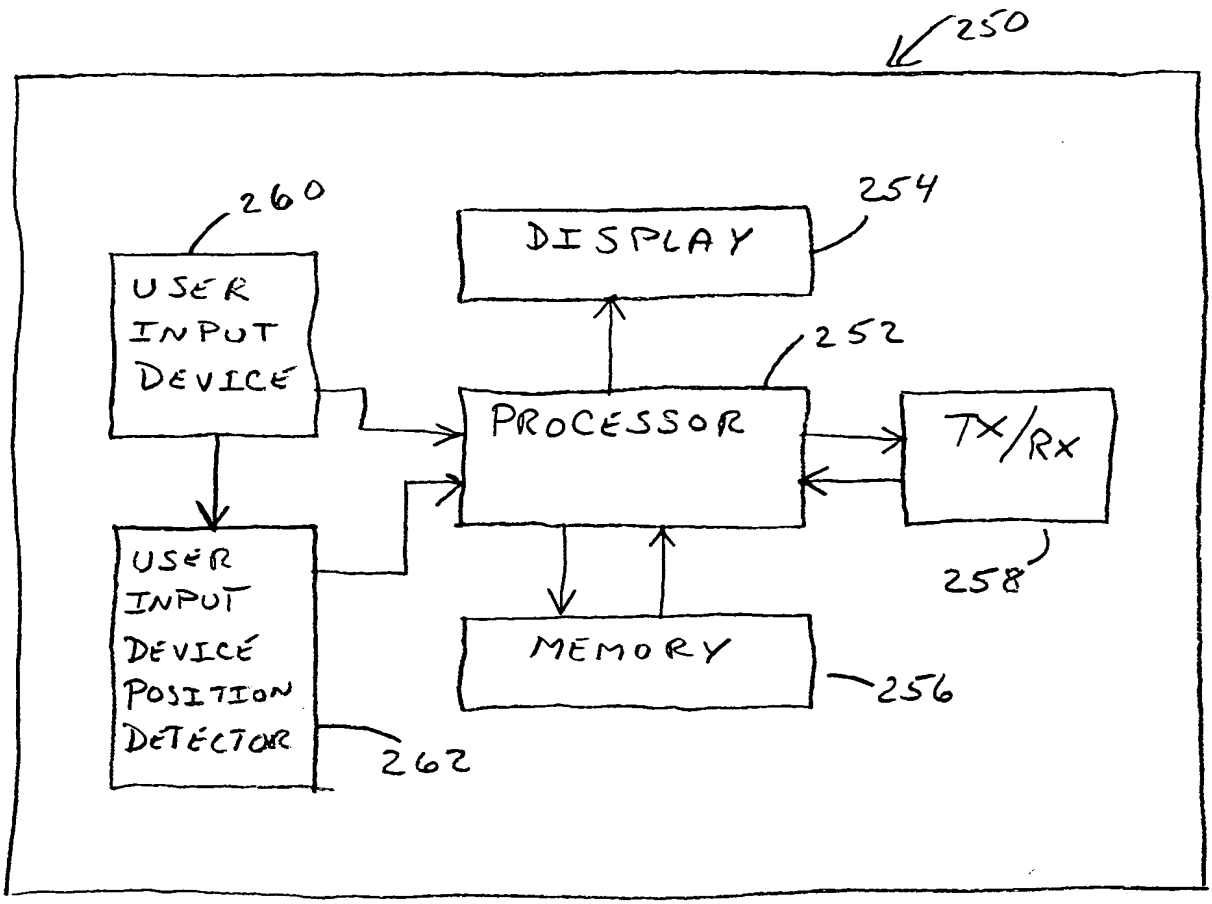

250
260
USER INPUT DEVICE
DISPLAY
254
252
PROCESSOR
TX/RX
258
USER INPUT DEVICE POSITION DETECTOR
262
MEMORY
256

FIG. 6